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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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09/808,957

03/16/2001

Hisao Hayashi

SON-2050

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7590

11/23/2004

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EXAMINER

LEWIS, MONICA

ART UNIT

PAPER NUMBER

2822

DATE MAILED: 11/23/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

Advisory Action	Application No. 09/808,957	Applicant(s) HAYASHI, HISAO	
	Examiner Monica Lewis	Art Unit 2822	

--The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

THE REPLY FILED 22 October 2004 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE. Therefore, further action by the applicant is required to avoid abandonment of this application. A proper reply to a final rejection under 37 CFR 1.113 may only be either: (1) a timely filed amendment which places the application in condition for allowance; (2) a timely filed Notice of Appeal (with appeal fee); or (3) a timely filed Request for Continued Examination (RCE) in compliance with 37 CFR 1.114.

PERIOD FOR REPLY [check either a) or b)]

- a) ☒ The period for reply expires 3 months from the mailing date of the final rejection.
- b) ☐ The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection.
ONLY CHECK THIS BOX WHEN THE FIRST REPLY WAS FILED WITHIN TWO MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f).

Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

1. ☐ A Notice of Appeal was filed on _____. Appellant's Brief must be filed within the period set forth in 37 CFR 1.192(a), or any extension thereof (37 CFR 1.191(d)), to avoid dismissal of the appeal.
2. ☐ The proposed amendment(s) will not be entered because:
- (a) ☐ they raise new issues that would require further consideration and/or search (see NOTE below);
 - (b) ☐ they raise the issue of new matter (see Note below);
 - (c) ☐ they are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or
 - (d) ☐ they present additional claims without canceling a corresponding number of finally rejected claims.

NOTE: _____

3. ☐ Applicant's reply has overcome the following rejection(s): _____.
4. ☐ Newly proposed or amended claim(s) _____ would be allowable if submitted in a separate, timely filed amendment canceling the non-allowable claim(s).
5. ☒ The a) ☐ affidavit, b) ☐ exhibit, or c) ☒ request for reconsideration has been considered but does NOT place the application in condition for allowance because: See Continuation Sheet.
6. ☐ The affidavit or exhibit will NOT be considered because it is not directed SOLELY to issues which were newly raised by the Examiner in the final rejection.
7. ☒ For purposes of Appeal, the proposed amendment(s) a) ☐ will not be entered or b) ☒ will be entered and an explanation of how the new or amended claims would be rejected is provided below or appended.

The status of the claim(s) is (or will be) as follows:

Claim(s) allowed: _____.

Claim(s) objected to: _____.

Claim(s) rejected: 22-28, 31-37, 40-46 and 49-52.

Claim(s) withdrawn from consideration: _____.

8. ☐ The drawing correction filed on _____ is a) ☐ approved or b) ☐ disapproved by the Examiner.
9. ☐ Note the attached Information Disclosure Statement(s) (PTO-1449) Paper No(s). _____.
10. ☐ Other: _____

Continuation of 5. does NOT place the application in condition for allowance because: First, Applicant argues that "the assertion found within the Office Action of a substrate comprised of an organic material and a metal fails to accurately represent the language found within the claims...the claimed invention provides that the product substrate is one of an organic material and a metal." The claim as disclosed above is open ended meaning that the substrate has to at least comprise either an organic material or a metal. The claim does not preclude the substrate from comprising additional elements. Second, Applicant argues that the "Office Action fails to show that the plastic substrate 11 of Toshiba is a suitable replacement for the nitride layer 36 of Vu, especially taking into consideration the presence within Vu of the plastic substrate 110." However, Applicant has stated that the product substrate has a "characteristic suitable for direct mounting of the thin film transistor" (See Specification-Abstract). The release layer in Vu is a product substrate because a thin film transistor (51) is mounted upon it (For Example: See Figure 4H). Additionally, the substrate (11) in Toshiba is a product substrate because a thin film transistor is mounted upon it (For Example: See Figure 5). Therefore, the substrates are suitable replacements for each other. Finally, Applicant argues that the "Office Action contends that the claimed feature of an adhesive layer being dissolvable to remove the manufacturing substrate makes that feature a product-by-process feature. In response to this contention, the adhesive layer being dissolvable to remove the manufacturing substrate is a structural feature of the adhesive layer." However, the adhesive layer being dissolvable to remove it is only a structural feature in so far as the layer must be removed. The method by which it is removed, i.e., dissolved, is a process limitation.



Mary Wilczewski
Primary Examiner